

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	23	((STUART) near2 (RYAN)).INV.	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/29 09:29
L2	16	((STUART) near2 (RYAN)).INV.	EPO; JPO; DERWENT	OR	ON	2010/03/29 09:29
L3	364	((ANDREW) near2 (JONES)). INV.	US-PGPUB; USPAT; USOCR	OR	ON	2010/03/29 09:29
L4	152	((ANDREW) near2 (JONES)). INV.	EPO; JPO; DERWENT	OR	ON	2010/03/29 09:29
L5	541	L1 L2 L3 L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:29
L6	4673	(select\$3 multiplex\$3) same (address with ((off near1 chip) (off-chip)external))same (address with ((on near1 chip) (on-chip) internal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:29
L7	324801	(interface controller (control near2 unit) bridge router) with ((off near1 chip) (off-chip) external)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:29
L8	2274	L6 and L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:29
L9	2	L5 and L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:29
L10	73772	address with map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:33

L11	1	9 and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:34
L12	272	712/38-39.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L13	7952	711/202.ccls. 711/154.ccls. 711/5.ccls. 711/211.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L14	1880	365/189.02.ccls. 365/230.02. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L15	5628	710/305.ccls. 710/316.ccls. 710/9.ccls. 710/14.ccls. 710/51. ccls. 710/38.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L16	15424	L12 L13 L14 L15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L17	348	L16 and L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L18	195	L7 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L19	168	L18 and (@ad<="20030517" @rlad<="20030517")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35

L20	73772	address with map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L21	208624	address with (rang\$3 chain group area)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L22	82	L19 and L20 and L21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:35
L23	449	(interface controller (control near2 unit) bridge router) same ((off near1 chip) (off-chip) external) same (parallel concurrent\$2 simultaneous\$2) same pin same reduc\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:39
L24	0	22 and 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:39
L25	2794	(interface controller (control near2 unit) bridge router) same ((off near1 chip) (off-chip) external) same (parallel concurrent\$2 simultaneous\$2) same pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:40
L26	2	22 and 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:40
L27	65	((second two plurality multiple) with map\$4) same chip same (on internal) same (off external)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:47
L28	0	26 and 27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:47

L29	2	25 and 27 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:48
L30	2	29 and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:49
L31	0	30 and 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:50
L32	6	(register with address with (control controlling)) same reallocate\$3 same (map\$4 with address)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:54
L34	0	32 and 27 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 09:58
L35	8649	711/202.ccls. 711/154.ccls. 711/5.ccls. 711/211.ccls. 711/ e12.058.ccls. 711/e12.081.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:11
L36	147	35 and L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:12
L37	84	L7 and 36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:12
L38	68	37 and (@ad<="20030517" @rlad<="20030517")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:13

L39	28	38 and L20 and L21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:13
L40	1	39 and 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:14
L41	0	40 and 27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/03/29 10:14

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L42	175491	("40" and "27").clm.	USPAT; UPAD	OR	ON	2010/03/29 10:31
L43	4	((register with address with (control controlling)) same reallocat\$3 same (map\$4 with address)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:32
L44	8	((interface controller (control near2 unit) bridge router) same ((off near1 chip) (off-chip) external) same (parallel concurrent\$2 simultaneous\$2) same pin same reduc\$3).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:33
L45	598	((select\$3 multiplex\$3) same (address with ((off near1 chip) (off-chip) external))same (address with ((on near1 chip) (on-chip) internal))).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:33
L46	9	((second two plurality multiple) with map\$4) same chip same (on internal) same (off external)).clm.	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:34
L47	0	43 and 45 and 46	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:34
L48	0	44 and 45 and 46	US-PGPUB; USPAT; UPAD	OR	ON	2010/03/29 10:35

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